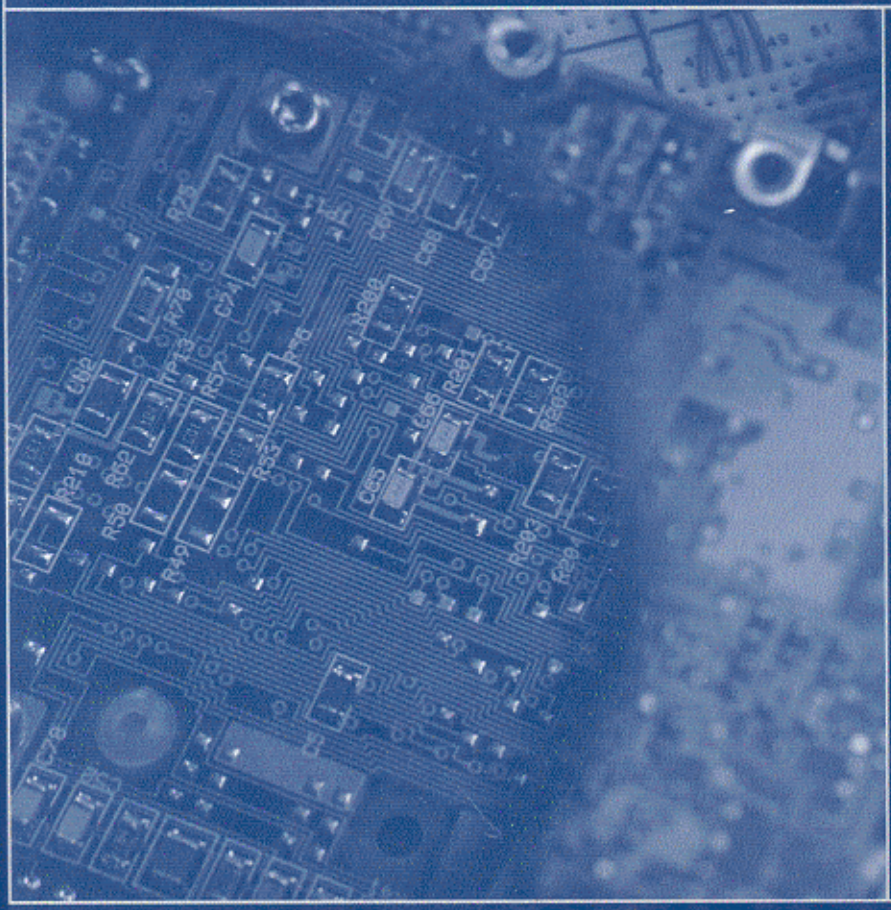
Hardware Design Verification

HDV

Simulation and Formal Method-Based Approaches



Wiliam K. Lam

Prentice Hall Modern Semiconductor Design Series

Contents